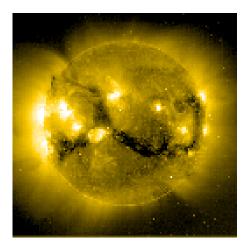
# Photovoltaic cell and module physics and technology

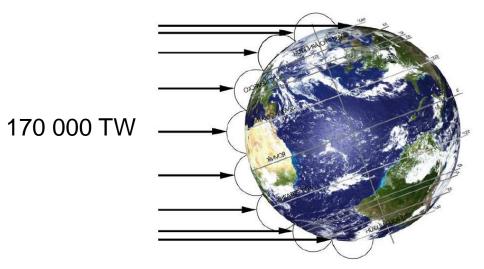
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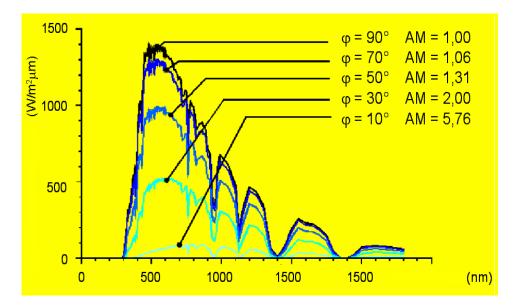
# Outlines

- Photovoltaic Effect
- Photovoltaic cell structure and characteristics
- Photovoltaic cell construction and technology
- PV modules construction and technology
- Summary

#### Solar energy







Photovoltaics

Direct transformation energy of solar irradiation into electricity

#### 1. Light absorption in materials and excess carrier generation

Photon energy  $hv = hc/\lambda$  (h is the Planck constant) photon momentum  $\approx 0$ 

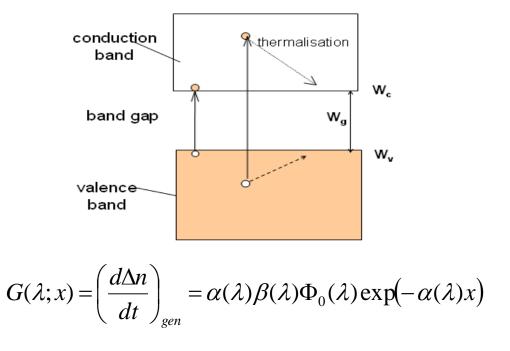
Light is absorbed in the material.

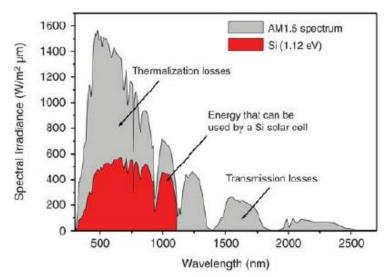
 $\Phi(x) \text{ is the light intensity} \qquad \Phi(x) = \Phi_0 \exp(-\alpha x) = \Phi_0 \exp\left(-\frac{x}{x_L}\right)$  $\alpha = \alpha(\lambda) \text{ is the absorption coefficient}$  $x_L = \frac{1}{\alpha} \text{ is the so-called absorption length} \qquad \int_0^{x_L} \Phi(x) dx = 0.68 \int_0^{\infty} \Phi(x) dx$ 

Absorption is due to interactions with material particles (electrons and nucleus). If particle energy before interaction was  $W_1$ , after photon absorption is  $W_1 + hv$ 

- interactions with the lattice -results in an increase of temperature
- interactions with free electrons results also in temperature increase
- interactions with bonded electrons- the incident light may generate some excess carriers (electron/hole pairs)

#### At interaction with photons of energy $h\nu \ge W_g$ electron-hole pairs are generated and carrier generation increases





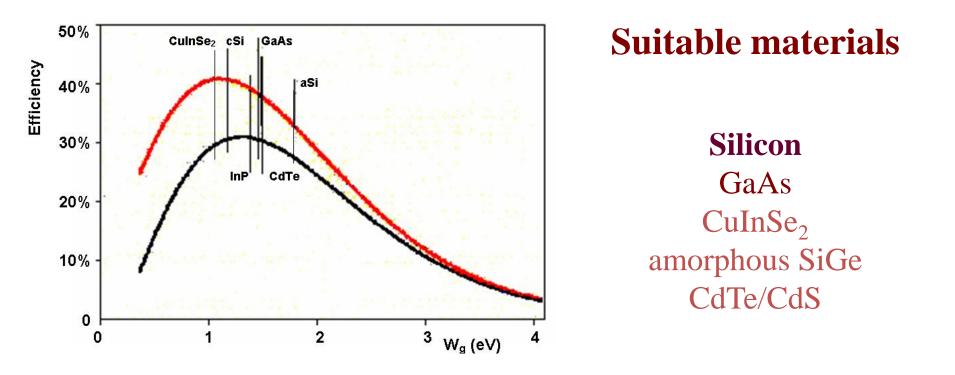
$$n = n_0 + \Delta n$$
,  $p = p_0 + \Delta p$ 

Excess carriers recombine with the recombination rate  $\tau$  is so called carrier lifetime

In dynamic equilibrium  $\Delta n = \Delta p = \tau G$ 

$$R = \left(\frac{d\Delta n}{dt}\right)_{rec} = -\frac{\Delta n}{\tau}$$

Efficiency of excess carrier generation by solar energy depens on the semiconductor band gap



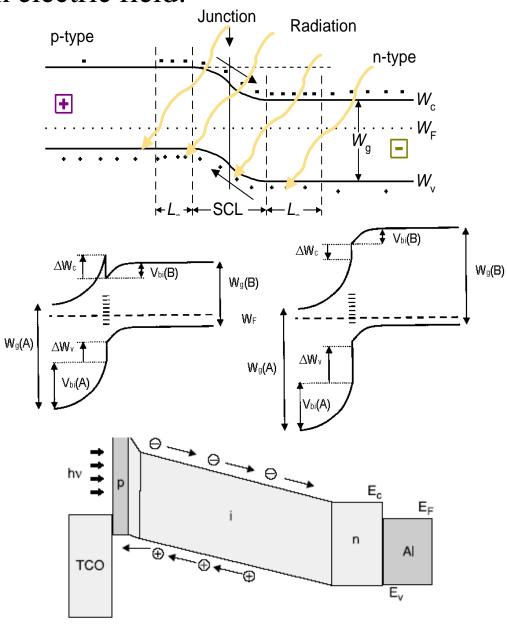
To obtain a potential difference that may be used as a source of electrical energy, an inhomogeneous structure with internal electric field is necessary.

#### Suitable structures with built-in electric field:

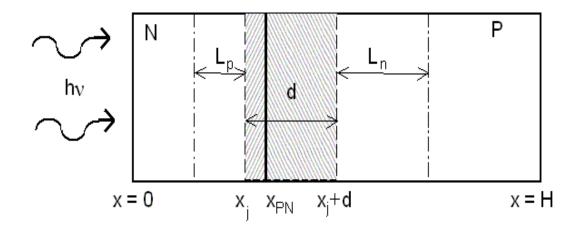
• PN junction

• heterojunction (contact of different materials).

• PIN structures



#### **Principles of solar cell function**



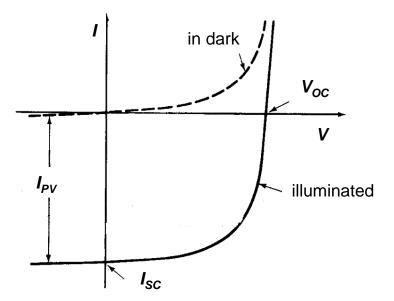
In the illuminated area generated excess carriers diffuse towards the PN junction. The density  $J_{PV}$  is created by carriers collected by the built-in electric field region

$$J_{PV}(\lambda) = q \int_{0}^{H} G(\lambda; x) dx - q \int_{0}^{H} \frac{\Delta n}{\tau} dx - J_{sr}(0) - J_{sr}(H)$$
  
$$J_{sr} \text{ is surface recombination}$$

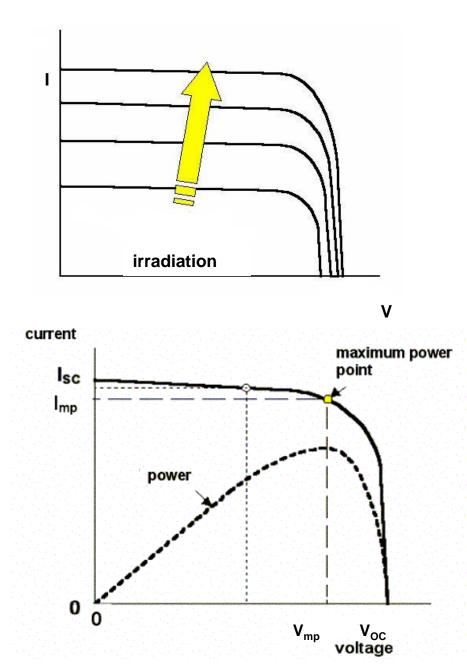
Total generated current density

$$J_{PV} = \int J_{PV}(\lambda) d\lambda$$

Illuminated PN junction:Asupperposition of photo-generatedcurrent and PN junction (dark)I-V characteristic

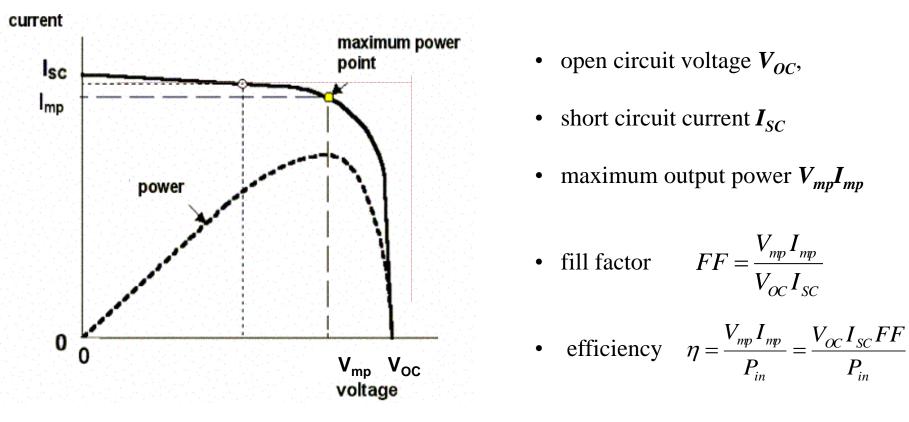


# Solar cell I-V chacteristic and its importan points



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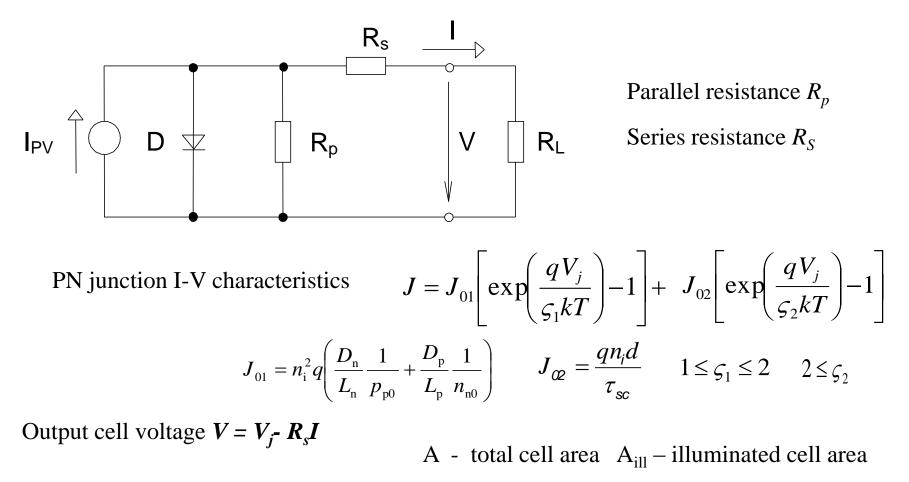
#### **Important solar cell electrical parameters**



All parameters  $V_{OC}$ ,  $I_{SC}$ ,  $V_{mp}$ ,  $I_{mp}$ , FF and  $\eta$  are usually given for standard testing conditions (STC):

- spectrum AM 1.5
- radiation power 1000 W/m<sup>2</sup>
- cell temperature 25°C.

#### Modelling I-V characteristics of a solar cell



$$I = A_{ill}J_{PV} - AJ_{01}\left[\exp\left(q\frac{V+R_sI}{\varsigma_1kT}\right) - 1\right] - AJ_{02}\left[\exp\left(q\frac{V+R_sI}{\varsigma_2kT}\right) - 1\right] - \frac{V+R_sI}{R_p}$$

#### **Influence of temperature**

For a high  $R_p$   $V_{OC} \approx \frac{kT}{q} \ln \frac{I_{PV}}{I_{01}}$  $I_{01} \sim n_i^2 = BT^3 \exp\left(\frac{-W_g}{kT}\right)$ 

Consequently

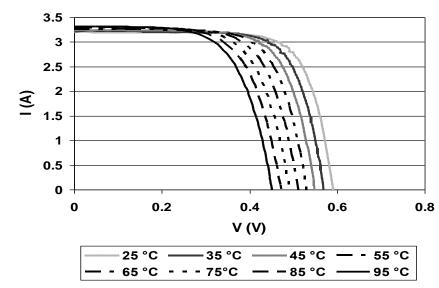
$$\frac{\partial V_{oC}}{\partial T} < 0$$

For silicon cells the decrease of  $V_{OC}$  is about 0.4%/K

Both fill factor and efficiency decrease with temperature

$$\frac{\partial FF}{\partial T} < 0 \qquad \frac{\partial \eta}{\partial T} < 0$$

At silicon cells 
$$\frac{1}{\eta} \frac{\partial \eta}{\partial T} \approx 0.5\% K^{-1}$$

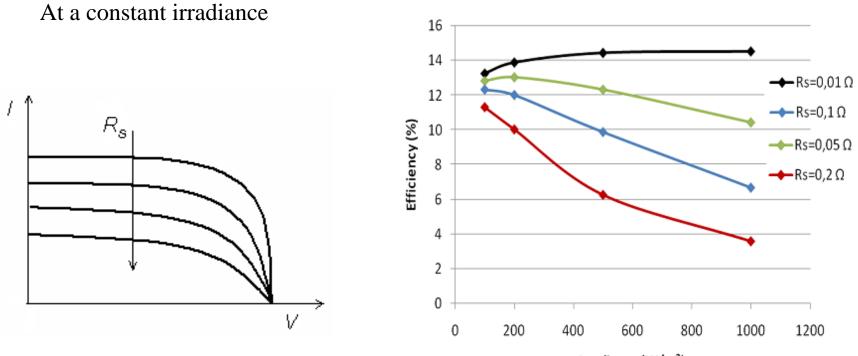


 $R_s$  increases with increasing temperature

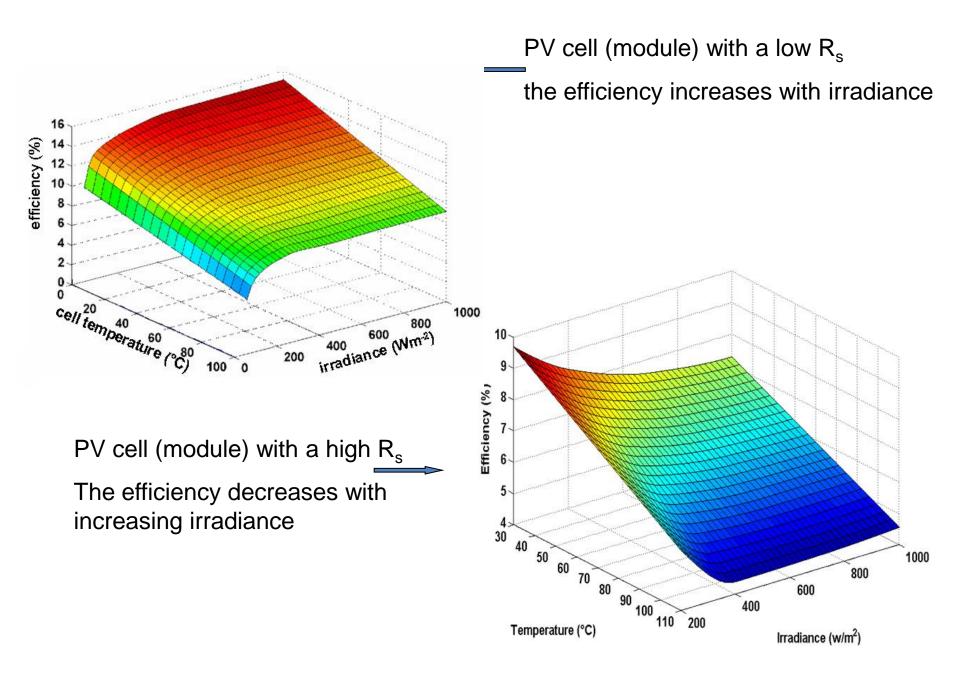
 $R_p$  decreases with increasing temperature

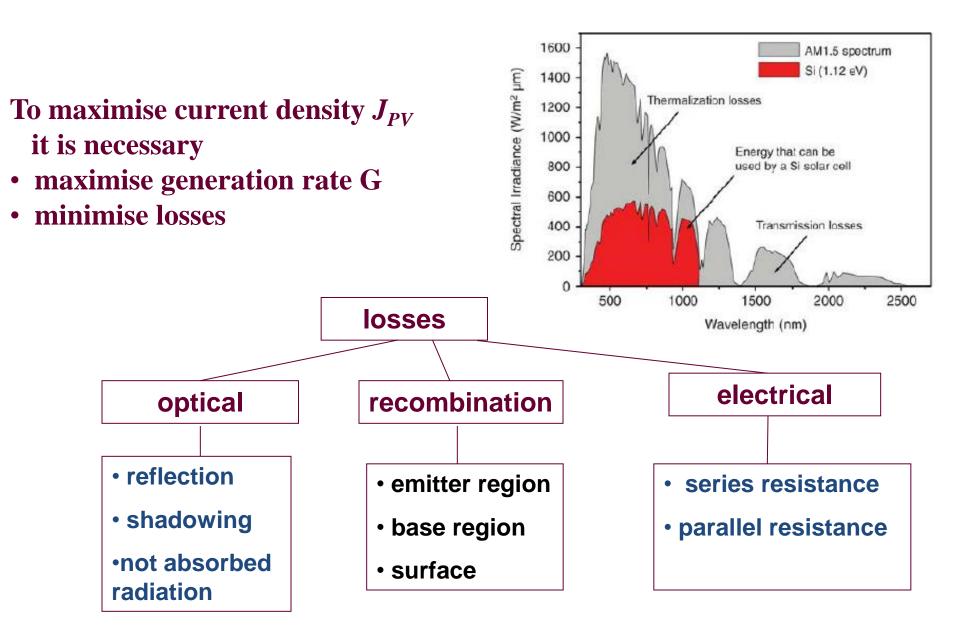
Cell type	$\eta$ (28°C)	$(1/\eta)(d\eta/dT)$ [×10 <sup>-3</sup> /°C]
Si	0.148	-4.60
Ge	0.090	-10.1
GaAs/Ge	0.174	-1.60
InP	0.195	-1.59
a-Si	0.066	-1.11 (nonlinear)
CuInSe <sub>2</sub>	0.087	-6.52

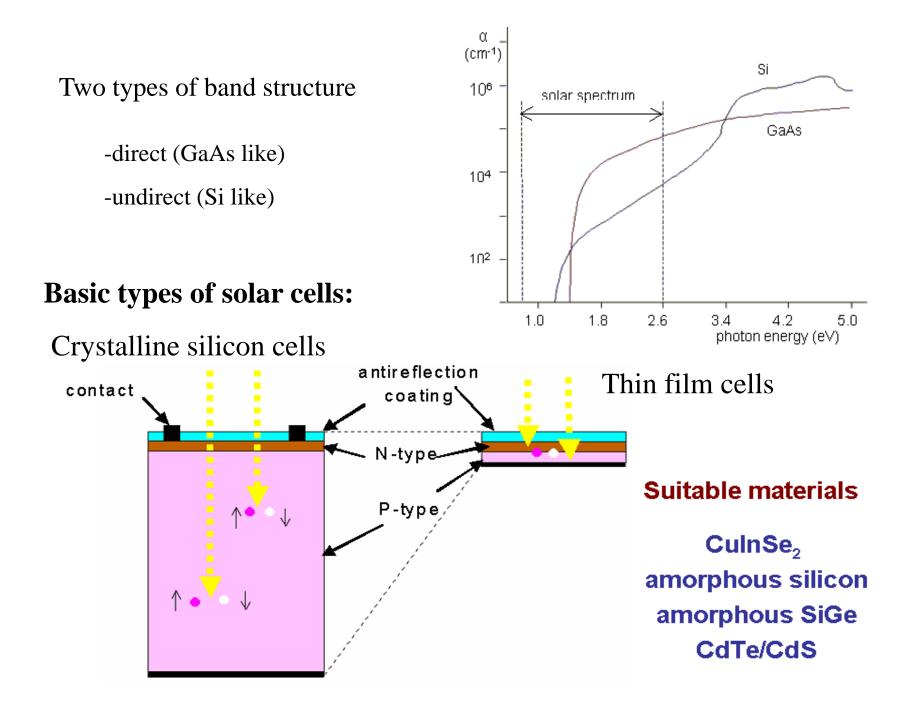
The series resistance  $R_s$  influences the cell efficiency



Irradiance (W/m<sup>2</sup>)

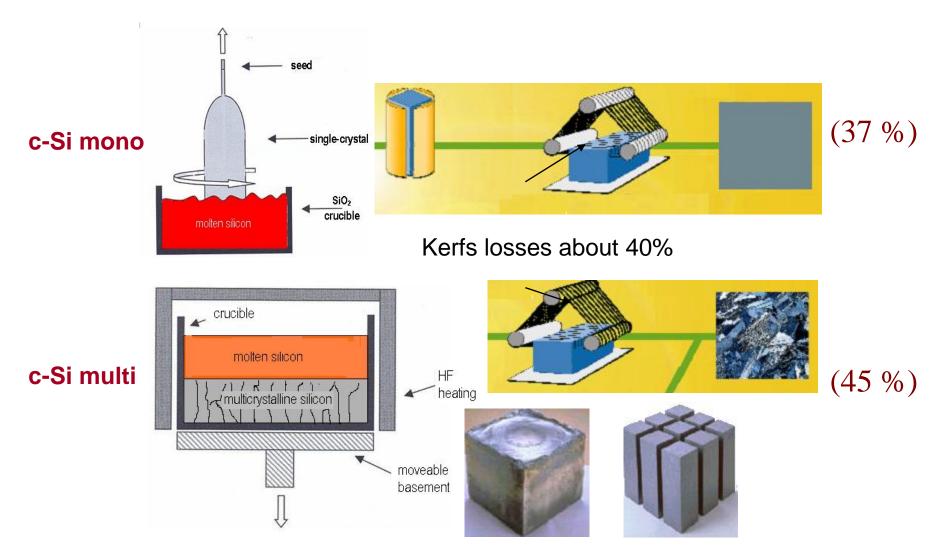






#### PV cells and modules from crystalline silicon (c-Si)

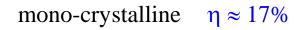
PV cells are realised from crystalline silicon wafers of thickness 0,15 - 0,25 mm and sides of 100 - 200 mm

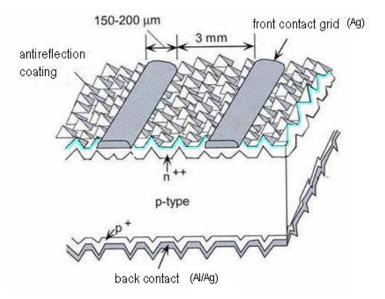


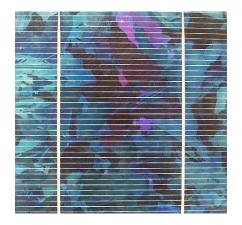
### Standard mass production (c-Si cells)

- starting P-type wafers
- chemical surface texturing
- phosphorous diffusion
- SiN(H) antireflection surface coating and passivation
- contact grid realised by the screen print technique
- contact firing
- edge grinding
- cell measuring and sorting



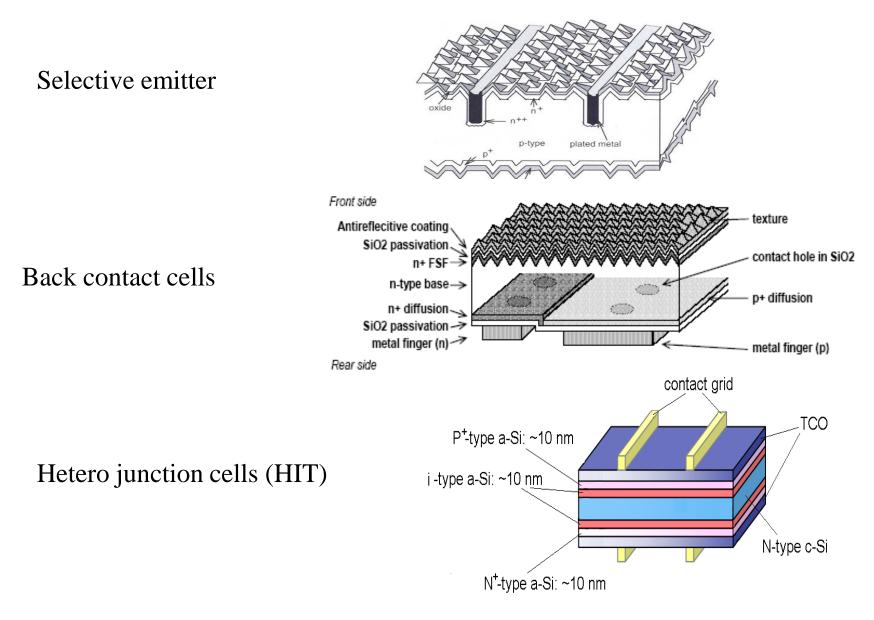






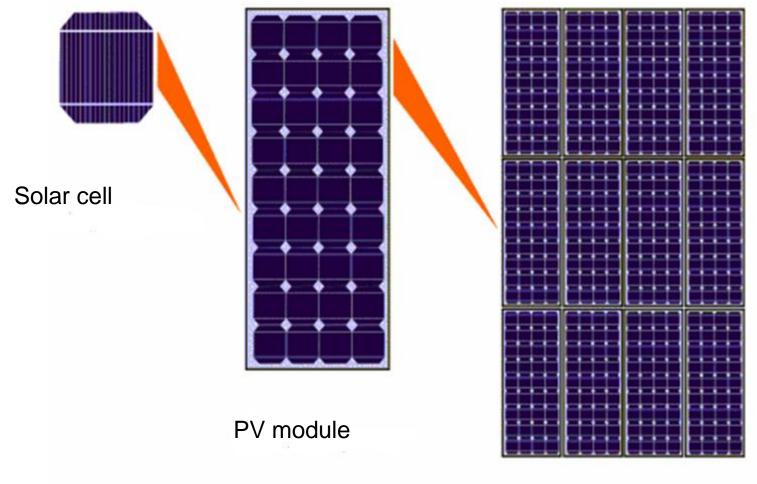
multi-crystalline  $\eta \approx 16\%$ 

#### Increasing cell efficiency



#### A single solar cell.....~0.5 V, about 30 mA/cm<sup>2</sup>

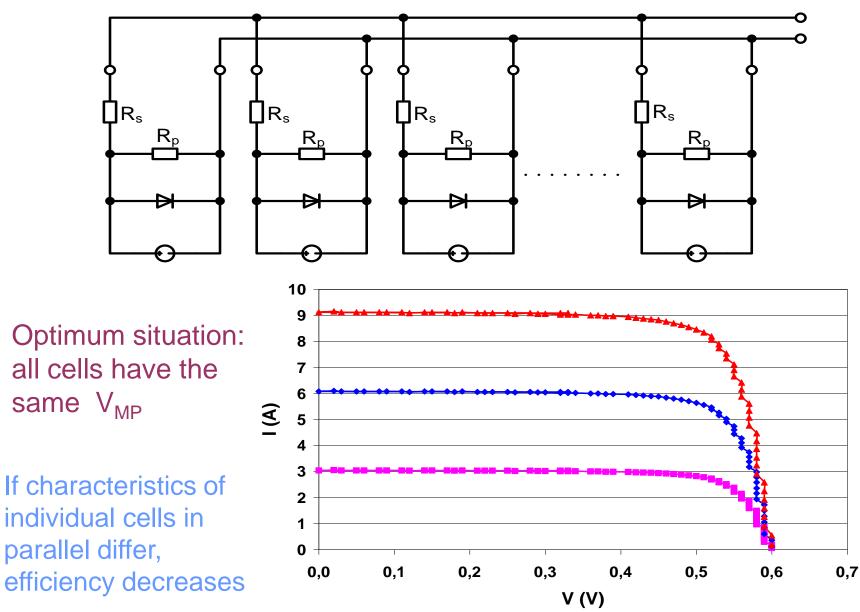
For practical use it is necessary connect cells in series to obtain a source of higher voltage and in parallel to obtain a higher current



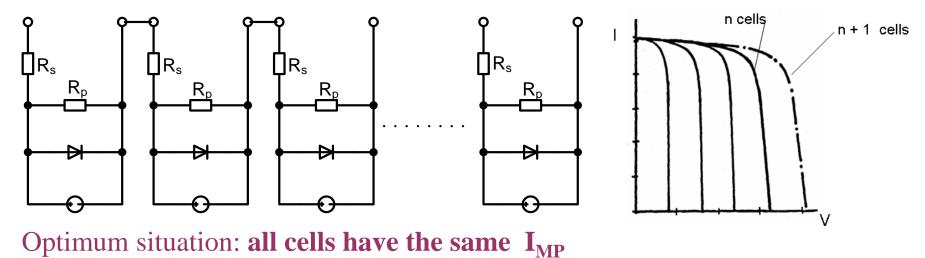
PV field

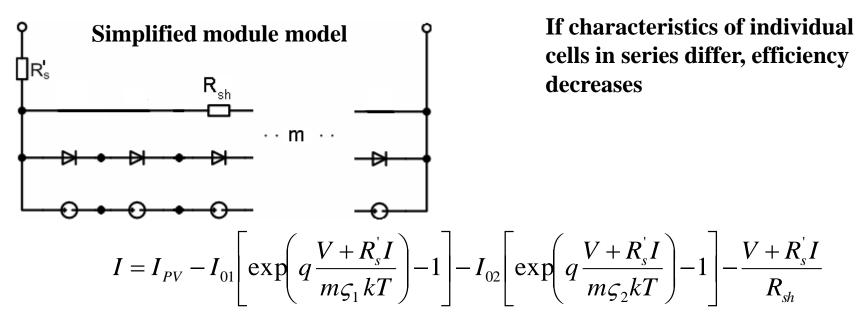
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#### **Cell connection in parallel**

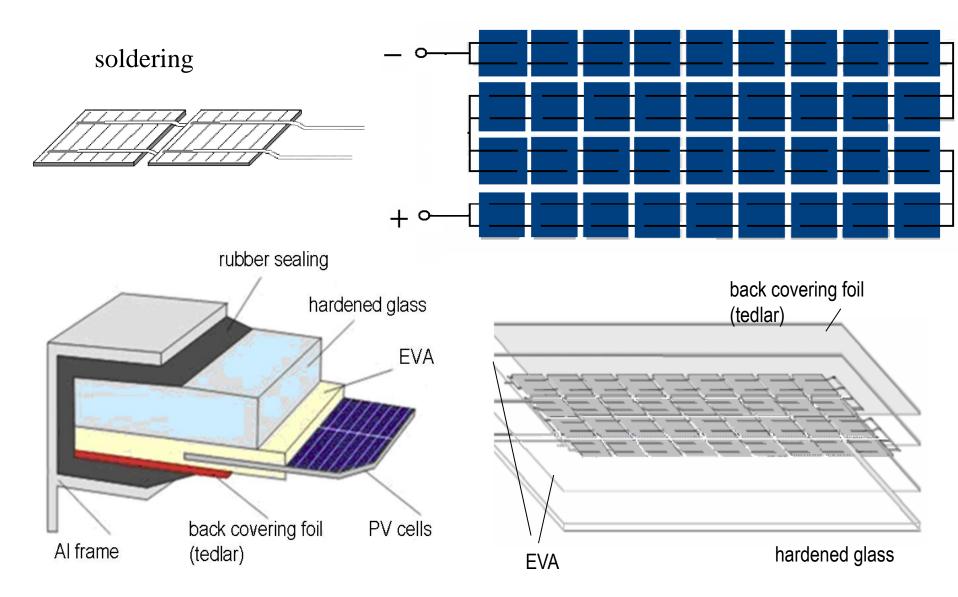


Cells in series..... the same current flows through all cells voltage does sums





### PV c-Si module technology



#### Module parameters

- open circuit voltage V<sub>OC</sub>,
- short circuit current  $I_{SC}$
- maximum output power  $V_{mp}I_{mp}$

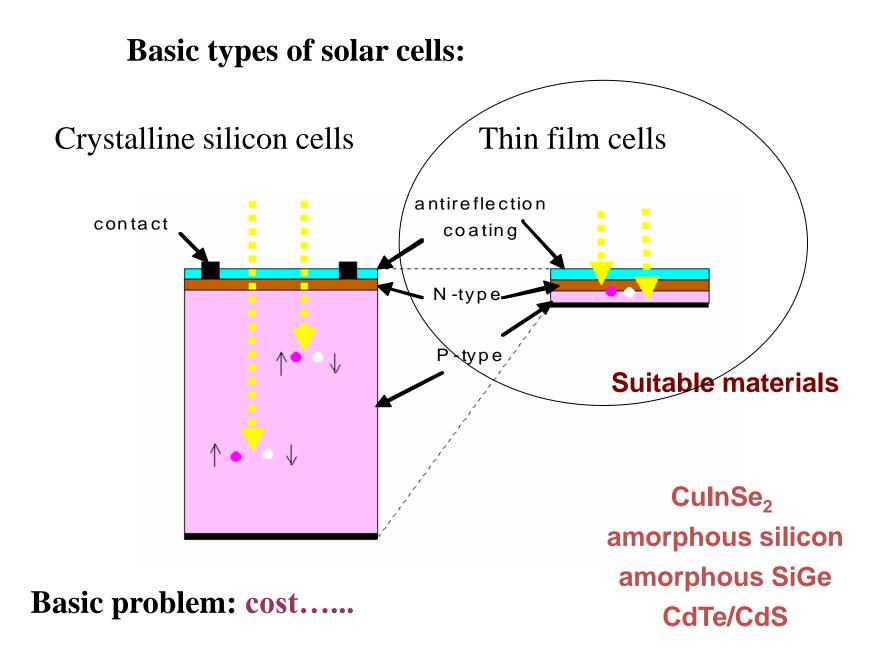
• fill factor 
$$FF = \frac{V_{mp}I_{mp}}{V_{OC}I_{SC}}$$

• efficiency 
$$\eta = \frac{V_{np}I_{np}}{P_{in}} = \frac{V_{OC}I_{SC}FF}{P_{in}}$$

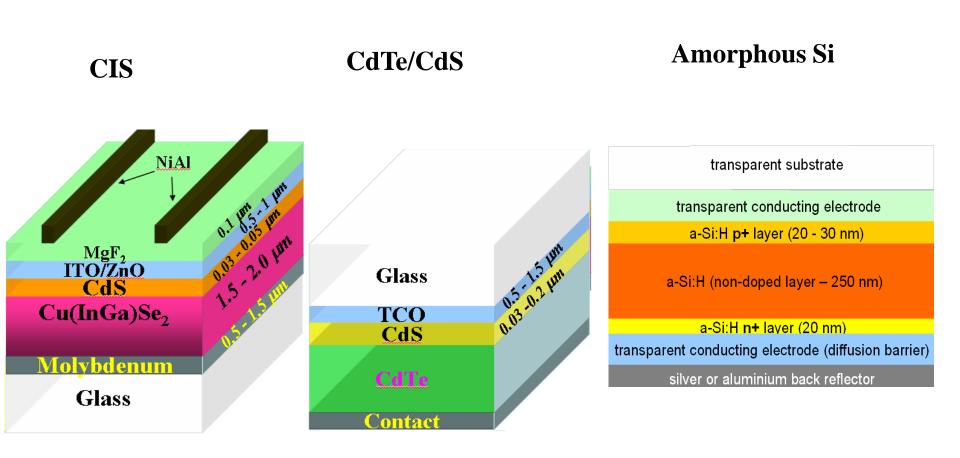
rubber sealing hardened glass EVA EVA back covering foil PV cells Al frame (tedlar)

STC (25°C, 1kW/m<sup>2</sup>, AM 1,5)

**NOCT** (Nominal Operating Conditions Temperature) Ambient temperature 20°C, 800 W/m<sup>2</sup>, wind 1 m/s



#### Thin film solar cells



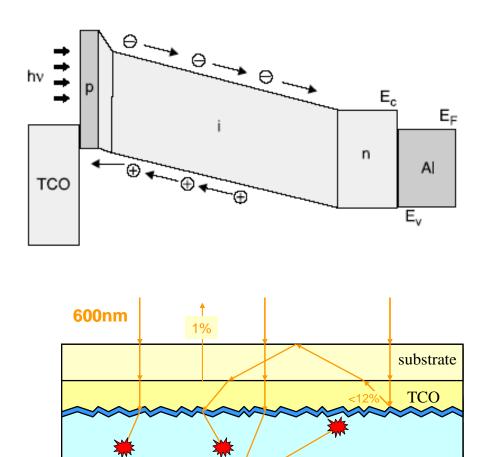
#### Market share:

1.5%

5.7%

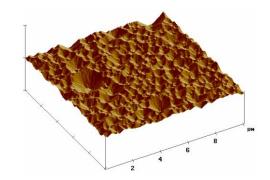
4.7%

#### Amorphous silicon solar cells



TCO

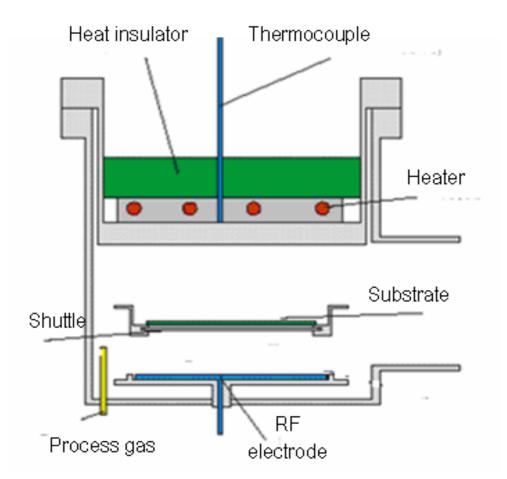
### TCO: SnO<sub>2</sub> ITO (indium-tin oxide) ZnO



**Light trapping** 

Plasma enhanced CVD (PECVD)

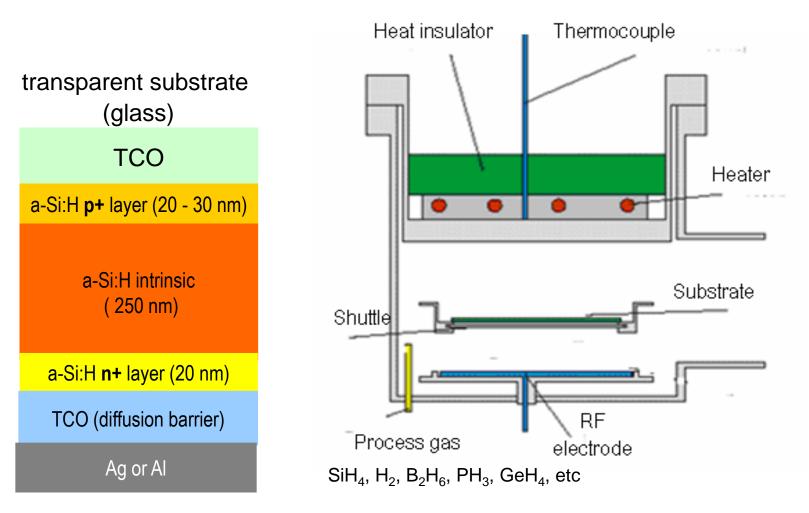
RF electrode and substrate create the capacitor structure. In this space the plasma and incorporated deposition of material on substrate takes place



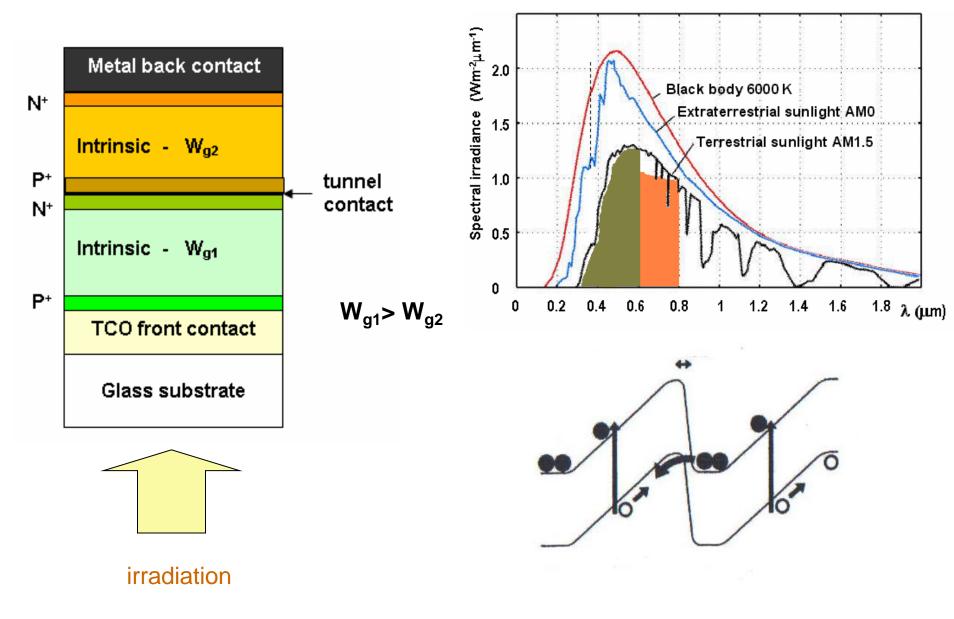
deposition of silicon nitride  $3SiH_4 + 3NH_3 \rightarrow Si_3N_4 + 12H_2$ deposition polysilicon layers  $SiH_4 \rightarrow Si + 2H_2$ .

### Thin film solar cell technology

#### Amorphous (microcrystalline) silicon solar cells

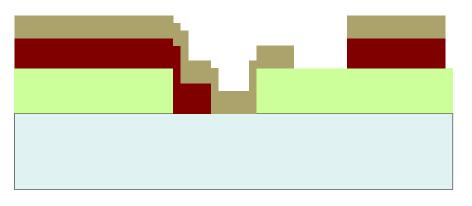


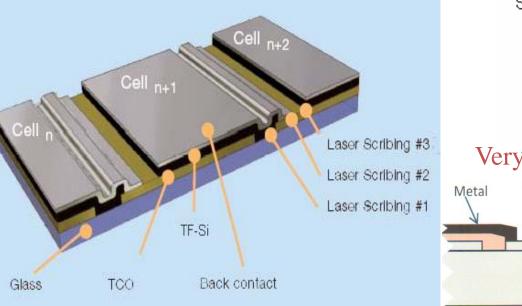
#### **Tandem cells**

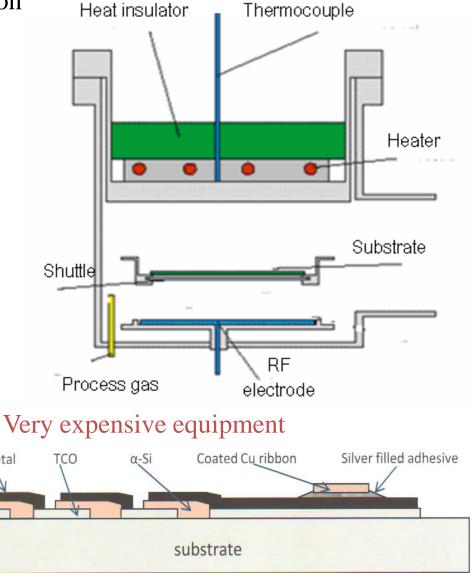


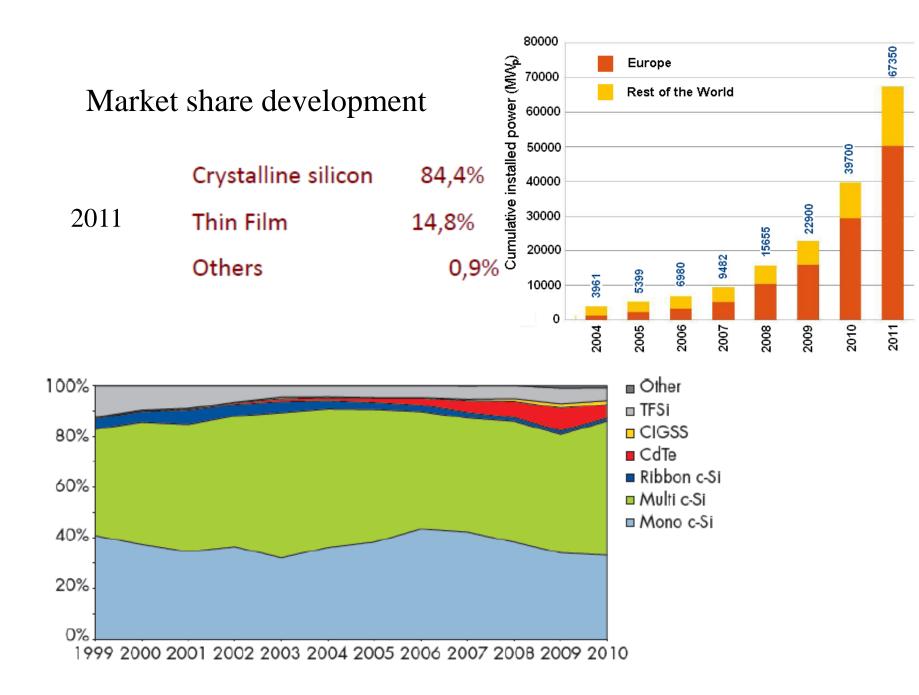
### Thin film modules on glass substrates

The module area is limited by the reaction chamber volume

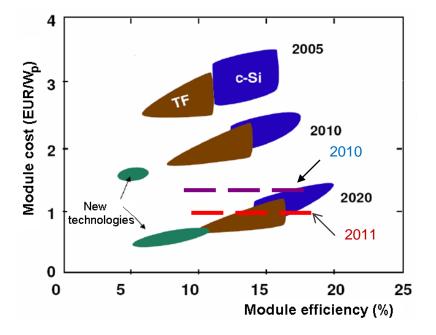




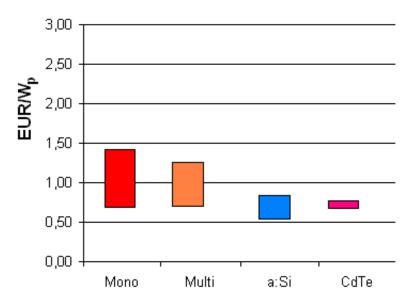




## PV module cost development



Reduction of silicon cost 2008..... 500 USD/kg 2010.....55 USD/kg 2012..... 22 USD/kg



Reduction of C-Si module cost

Thin-film modules are not cheaper than modules from crystalline silicon